Nepal College of Information Technology

**Unit Test**

Spring 2013

Program : BE ELX. Time : 2 hrs

Semester : Spring (VI) FM : 70

Subject : Computer Organization PM : 30

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| *Candidates are required to give their answers in their own words as far as practicable.* |
| *The figures in the margin indicate full marks.* |
| Attempt all the questions. |

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|  | 1. What are purposes of different types of registers in IAS computer? 2. Multiply (-9) × (-13) using Booth's Algorithm. | 5  10 |
|  | 1. What is the overall function of processor’s control unit? 2. What do you mean by addressing mode? Describe any four addressing modes with their advantages and disadvantages | 7  8 |
|  | 1. Draw instruction cycle state diagram and explain? 2. Differentiate hardwired implementation and micro programmed control techniques for control unit design | 7  8 |
|  | 1. What is the difference between zero-address, one address and two address instructions? 2. What do you mean by micro-operation? Illustrate the micro operations involved in fetch and indirect sub cycles of an instruction.. | 8  7 |
|  | Write short notes on **(Any Two)** :   1. IEE754 Standard 2. Types of registers 3. Instruction set design Issues | 5×2 |